|  |  |  |  |
| --- | --- | --- | --- |
| **Laboratory Report** | | | |
| **Laboratory Exercise No.:** | 5 | **Date Performed:** | March 14, 2023 |
| **Laboratory Exercise Title:** | Analog to Digital Converter | | |
| **Name of Students:** | Danica Marie A. Dumalagan  with: Jhury Kevin P. Lastre &  Paul John R. Toral | **Group:** | 4 |

**Laboratory Exercise #5-1:**

|  |  |  |
| --- | --- | --- |
| **Figure #** | **Input Voltage (V)** | **Output Voltage (V in bars)** |
| 1 | 4.999e-005 | 0 |
| 2 | 0.899 | 1 |
| 3 | 0.999 | 1 |
| 4 | 1.399 | 1 |
| 5 | 1.999 | 2 |
| 6 | 2.549 | 3 |
| 7 | 2.999 | 3 |
| 8 | 3.849 | 4 |
| 9 | 3.999 | 4 |
| 10 | 4.149 | 4 |
| 11 | 4.999 | 5 |

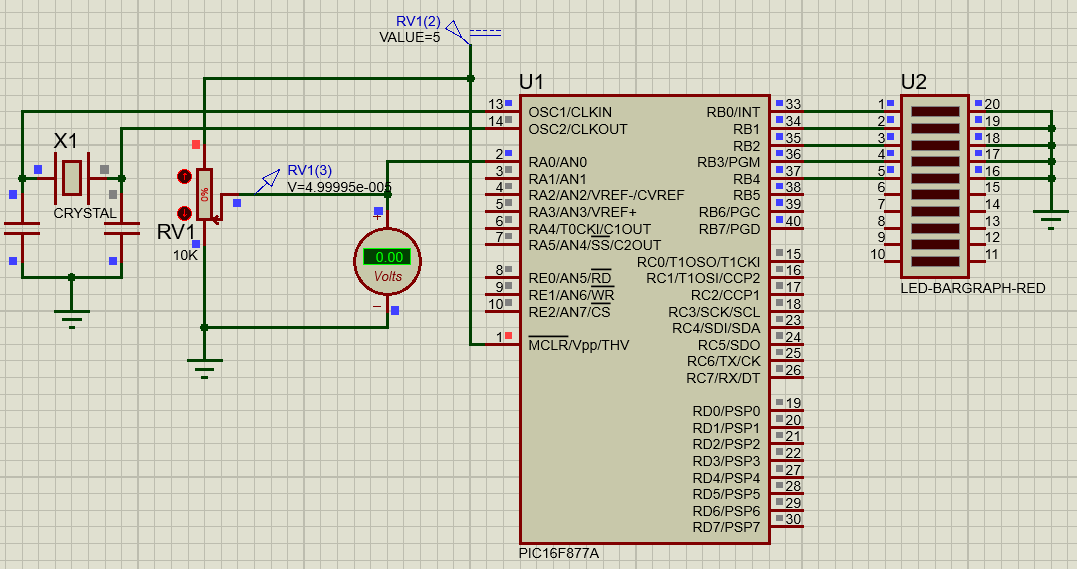


Figure 1.

Diagram, schematic

Description automatically generated

Figure 2.

Diagram, schematic

Description automatically generated

Figure 3.

Diagram, schematic

Description automatically generated with medium confidence

Figure 4.

Diagram, schematic

Description automatically generated

Figure 5.

Diagram, schematic

Description automatically generated

Figure 6.

Diagram, schematic

Description automatically generated

Figure 7.

Diagram, schematic

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Figure 8.

Diagram, schematic

Description automatically generated

Figure 9.

Diagram

Description automatically generated

Figure 10.

Diagram, schematic

Description automatically generated

Figure 11.

**Laboratory Exercise #5-2:**

|  |  |  |
| --- | --- | --- |
| **Figure #** | **Input Voltage (V)** | **Output Voltage (V)** |
| 12 | 0.050 | 0.0 |
| 13 | 0.449 | 0.4 |
| 14 | 0.999 | 1.0 |
| 15 | 1.649 | 1.6 |
| 16 | 1.999 | 2.0 |
| 17 | 2.199 | 2.2 |
| 18 | 2.999 | 3.0 |
| 19 | 3.649 | 2.6 |
| 20 | 4.049 | 4.0 |
| 21 | 4.439 | 4.3 |
| 22 | 4.999 | 5.0 |

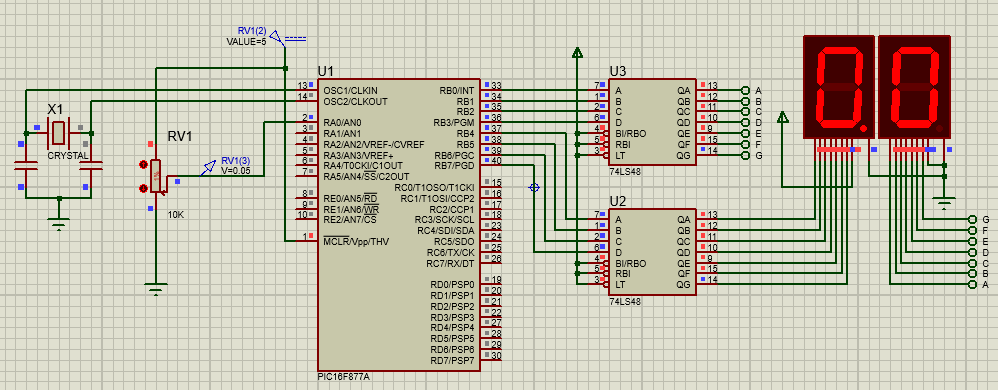
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Figure 12.

**Diagram, schematic

Description automatically generated**

Figure 13.

**Diagram, schematic

Description automatically generated**

Figure 14.

**Diagram, schematic

Description automatically generated**

Figure 15. Diagram, schematic

Description automatically generated

Figure 16.

Diagram, schematic

Description automatically generated

Figure 17.

Diagram, schematic

Description automatically generated

Figure 18.

Diagram, schematic

Description automatically generated

Figure 19.

Diagram, schematic

Description automatically generated

Figure 20.

Diagram, schematic

Description automatically generated

Figure 21.

Diagram, schematic

Description automatically generated

Figure 22.